ALP LAB

E1M-X V2N Errata

Known issues and advisories

Document Number: ER-V2N-001 **Revision:** 0.4 **Date:** November 2026 **Status:** Preliminary

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1 Scope

This document is the canonical errata list for the **E1M-X V2N family** of Alp Lab System-on-Modules (`E1M-V2N101`, `E1M-V2N102`, `E1M-V2N103`). It tracks hardware, firmware, and documentation issues against specific module-PCB and factory-image revisions.

This errata complements but does not replace the **Renesas RZ/V2N** SoC errata (published separately by Renesas). SoC-level issues are not duplicated here unless they materially affect the V2N module integration or have a different mitigation on the SoM.

2 Module-Revision History

Revision	Production status	Notes
Rev. A	Engineering samples only	Pre-production. Not for customer shipment.
Rev. B	Pilot run (Q1 2026)	First customer-shipped revision.

Table 1 Module-PCB revision history

3 Errata at Current Revision (Rev. B)

> **Note:** **No known errata at this revision.** The table below is reserved for future entries.

ID	Title	Affected SKUs	Affected rev.	Severity	Status / Workaround
—	—	—	—	—	—

Table 2 Active errata

4 Resolved Errata

> **Note:** No resolved errata to report at this time.

ID	Title	Affected SKUs	Affected rev.	Resolved in
—	—	—	—	—

Table 3 Resolved errata

5 Severity Conventions

Same as **ER-AEN-001 §5**. Critical / Major / Minor / Documentation.

6 Reporting a New Issue

If you believe you've identified an issue not listed above:

1. Confirm the module revision (laser-marked on the underside of the SoM).
2. Capture the symptom (logs, photos, oscilloscope traces, measurement conditions).
3. Note the **boot DIP-switch state** at the moment of failure — many V2N-class issues are misdiagnosed without this.
4. File a report through one of the channels in **DS-V2N-001 §18.4**.

Alp Lab will respond within five business days with an initial triage classification.

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7 Revision History (of This Document)

Revision	Changes	Date
0.1	• Initial published errata. • No active entries at this revision.	May 2026
0.2	• Scope: added `E1M-V2N103` (same silicon and module PCB as the other family SKUs; memory tier only).	October 2026
0.3	• Release history: every entry formatted as a bullet list.	October 2026
0.4	• Product name on page 1 links to the product page on alplab.ai.	November 2026

Table 4 Document revision history

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