ALP LAB

E1M-X V2N-M1 Errata

Known issues and advisories

Document Number: ER-V2N-M1-001 **Revision:** 0.4 **Date:** November 2026 **Status:** Preliminary

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1 Scope

This document is the canonical errata list for the **E1M-X V2N-M1 family** of Alp Lab System-on-Modules (`E1M-V2M101`, `E1M-V2M102`, `E1M-V2M103`). It tracks hardware, firmware, and documentation issues against specific module-PCB and factory-image revisions, including issues specific to the **DeepX DX-M1** on-module AI accelerator.

This errata complements but does not replace:

- The **Renesas RZ/V2N** SoC errata (published by Renesas).
- The **DeepX DX-M1** hardware errata (published by DeepX).
- The **E1M-X V2N Errata** (`ER-V2N-001`) — issues affecting the V2N base also affect V2N-M1 unless explicitly noted as DX-M1-specific.

2 Module-Revision History

Revision	Production status	Notes
Rev. A	Engineering samples only	Pre-production. Not for customer shipment.
Rev. B	Pilot run (Q2 2026)	First customer-shipped revision.

Table 1 Module-PCB revision history

3 Errata at Current Revision (Rev. B)

> **Note:** **No known errata at this revision.** All V2N-base errata are reflected in `ER-V2N-001`; this table is reserved for DX-M1-specific or multiplex-related issues that only manifest on the V2N-M1 SKUs.

ID	Title	Affected SKUs	Affected rev.	Severity	Status / Workaround
—	—	—	—	—	—

Table 2 Active errata

4 Resolved Errata

> **Note:** No resolved errata to report at this time.

ID	Title	Affected SKUs	Affected rev.	Resolved in
—	—	—	—	—

Table 3 Resolved errata

5 Severity Conventions

Same as `ER-AEN-001` §4. Critical / Major / Minor / Documentation.

6 Reporting a New Issue

If you believe you've identified an issue not listed above:

1. Confirm the module revision (laser-marked on the underside of the SoM).

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2. Capture the symptom (logs, photos, oscilloscope traces, measurement conditions).
3. Note the **boot DIP-switch state** and the **PCIe-mux state** (DX-M1 vs external M.2) at the moment of failure.
4. For DX-M1-specific issues, capture the `lspci -vvv` output of the DX-M1 device.
5. File a report through one of the channels in DS-V2N-M1-001 §18.4.

Alp Lab will respond within five business days with an initial triage classification.

7 Revision History (of This Document)

Revision	Changes	Date
0.1	• Initial published errata. • No active entries at this revision.	May 2026
0.2	• Scope: added E1M-V2M103 (same silicon and module PCB as the other family SKUs; memory tier only).	October 2026
0.3	• Release history: every entry formatted as a bullet list.	October 2026
0.4	• Product name on page 1 links to the product page on alplab.ai.	November 2026

Table 4 Document revision history

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